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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/758,203	01/16/2004	Hyo Jin Kim	0465-1140P	3218
2292	7590	06/25/2007	EXAMINER	
BIRCH STEWART KOLASCH & BIRCH			DO, CHAT C	
PO BOX 747			ART UNIT	PAPER NUMBER
FALLS CHURCH, VA 22040-0747			2193	
NOTIFICATION DATE		DELIVERY MODE		
06/25/2007		ELECTRONIC		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

mailroom@bskb.com

Office Action Summary	Application No.	Applicant(s)	
	10/758,203	KIM, HYO JIN	
	Examiner Chat C. Do	Art Unit 2193	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 16 January 2004.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-10 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-10 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 16 January 2004 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 - Certified copies of the priority documents have been received in Application No. _____.
 - Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date _____	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

Claim Objections

1. Claims 2, 5, and 7-8 are objected to because of the following informalities:

Re claim 2, the applicant is advised to write-out the acronym “ORC” in line 2 for clarification. Claims 7-8 have the same objection.

Re claim 5, the applicant is advised to rewrite the term “pile line” in line 4 as “pipeline” for correction and remove the number “270” in the last line of the claim.

Claim 7 also has the same objection.

Appropriate correction is required.

Claim Rejections - 35 USC § 101

2. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

3. Claims 1-10 are rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter.

Claims 1-10 cite a processor for processing floating-point data in accordance with a mathematical algorithm. In order for claims to be statutory, claims must either include a practical/physical application or a concrete, useful, and tangible result. However, claims 1-10 merely disclose steps/components for processing floating-point data without further disclosing a practical/physical application or a useful and tangible result since claims appear to preempt every substantial practical application of the idea embodied by

the claim and no limitations in claims that breathes sufficient life and meaning into the preamble so as to limit it to a particular practical application rather than being so broad and sweeping as to cover every substantial practical application of the idea embodied therein. Claims 1-10 are just pre-emptive every practical application. Therefore, claims 1-10 are directed to non-statutory subject matter.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claim 5 is rejected under 35 U.S.C. 102(e) as being anticipated by Hoyle et al. (U.S. 6,757,819).

Re claim 5, Hoyle et al. disclose in Figures 1 and 6-8 a digital signal processor supporting a floating point (e.g. abstract and Figure 8), comprising: a program memory for storing a program with a predetermined instruction (e.g. components 1030 and 10x in Figure 8); an instruction register for fetching (e.g. component 10a in Figure 8) and storing the instruction of the program (e.g. component 10b in Figure 8); a decoding and pipeline controller for decoding the instruction so as to control pile line needed for performing a correspondent instruction (e.g. component 10c in Figure 8 and col. 10 line 44 to col. 11 line 20); a data memory for storing data (e.g. col. 17 lines 15-18); a program address

generator for generating a program address to be currently performed and transmitting the program address to the program memory (e.g. Figure 8); a data address generator generating a data address for inputting and outputting the data memory through a control of the decoding and pipeline controller (e.g. col. 13 lines 1-10); an operating processor performing calculation, logic, multiplication for a data operation (e.g. component 710 in Figure 7A); and a multiplier for performing multiplication needed for the data operation (e.g. Figure 7A as multiplier).

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- a. U.S. Patent No. 5,095,455 to Hmida et al. disclose binary multiplier circuit with improved inputs.
- b. U.S. Patent No. 6,212,667 to Geer et al. disclose an integrated circuit test coverage evaluation and adjustment mechanism and method.
- c. U.S. Patent No. 6,959,314 to Californiaa discloses a method of translating Boolean algebra into basic algebra.
- d. U.S. Patent Application Publication No. 2002/0194240 to Pangal et al. disclose a floating point multiply accumulator.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chat C. Do whose telephone number is (571) 272-3721. The examiner can normally be reached on M => F from 7:00 AM to 5:30 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on (571) 272-3756. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Chat C. Do
Examiner
Art Unit 2193

June 19, 2007

A handwritten signature in black ink, appearing to read "Chat C. Do".